

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) ~~Display~~ A display device (3) comprising a display panel (2) having a plurality of display pixels (4) with emissive display elements (11); and duty cycle control means (7) for varying a fraction of a frame period during which said display pixels (4) emit light in dependence on ~~an overall~~ a total brightness level for a frame of an image to be displayed on said display panel (2).

2. (Currently Amended) ~~Display~~ The display device (3) according to claim 1, wherein said control means (7) are adapted to increase said fraction if a decrease of said ~~overall~~ total brightness level is determined.

3. (Currently Amended) ~~Display~~ The display device (3)

according to claim 1 wherein said control means ~~(7)~~ are adapted to decrease said fraction if an increase of said ~~overall-total~~ brightness level is determined.

4. (Currently Amended) ~~Display~~ The display device ~~(3)~~ according to claim 1, further comprising a frame memory for storing input signals, representing said image, during a frame period for enabling a determination of the ~~overall-total~~ brightness level of the image during the frame period.

5. (Currently Amended) ~~Display~~ The display device ~~(3)~~ according to claim 1, wherein the control means ~~(7)~~ are adapted to determine the fraction of the frame period in dependence on the ~~overall-total~~ brightness level of the image during a previous frame period.

6. (Currently Amended) ~~Display~~ The display device ~~(3)~~ according to claim 1, wherein the control means ~~(7)~~ further comprise a look-up table ~~(9)~~ for determining said fraction corresponding with said determined ~~overall-total~~ brightness level.

7. (Currently Amended) ~~Display~~ The display device (3) according to claim 1, wherein said display pixels (4) ~~comprise~~ a switch (T3) ~~coupled to said control means (7) for enabling light emission by said corresponding emissive display element (11) for~~ said fraction of said frame period.

8. (Currently Amended) ~~Method~~ A method for controlling a display panel (2) ~~having a plurality of display pixels (4) with emissive display elements (11) comprising the steps~~ acts of:

[[-]] ~~determining an overall~~ a total brightness level for a frame of an image to be displayed in a frame period on said display, and

[[-]] ~~controlling a fraction of said frame period during which said display pixels (4) emit light in dependence on said overall total~~ brightness level.

9. (Currently Amended) ~~Integrated~~ An integrated circuit for controlling a display panel (2) ~~having a plurality of display pixels (4) with emissive display elements (11), the integrated~~

circuit comprising a duty cycle control arrangement (7)—for varying a fraction of a frame period during which said display pixels (4) emit light in dependence on ~~an overall~~ a total brightness level for a frame of an image to be displayed on said display panel—(2).

10.(New) The display device of claim 1, wherein the duty cycle control means include an adder configured to add brightness values of levels of the plurality of display pixels for the frame to form the total brightness level for the frame.

11.(New) The display device of claim 10, wherein the duty cycle control means include memory configured to store the total brightness level for the frame.

12.(New) The display device of claim 1, wherein the duty cycle control means include an adder configured to add luminance values of color components of a signal representing the image to be displayed to form the total brightness level for the frame.

13.(New) The method of claim 8, wherein the determining act

includes adding brightness values of levels of the plurality of display pixels for the frame to determine the total brightness level for the frame.

14.(New) The method of claim 13, wherein the determining act includes storing the total brightness level for the frame.

15.(New) The method of claim 8, wherein the determining act includes adding luminance values of color components of a signal representing the image to be displayed to determine the total brightness level for the frame.

16.(New) The integrated circuit of claim 9, wherein the duty cycle control means include an adder configured to add brightness values of levels of the plurality of display pixels for the frame to form the total brightness level for the frame.

17.(New) The integrated circuit of claim 16, wherein the duty cycle control means include memory configured to store the total brightness level for the frame.

18.(New) The integrated circuit of claim 9, wherein the duty cycle control means include an adder configured to add luminance values of color components of a signal representing the image to be displayed to form the total brightness level for the frame.